Leonard Forbes et al.

Examiner: Michael Trinh

09/256,643

Group Art Unit: 2822

February 23, 1999

Docket: 303.324US2

Title:

TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AS

METHODS OF FABRICATION AND USE

PETITION FOR A ONE-MONTH EXTENSION OF TIME

Commissioner for Patents Washington, D.C. 20231

In accordance with the provisions of 37 C.F.R. § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Office Action mailed July 23, 2001, said period of response being extended from October 23, 2001 to November 23, 2001.

Our check in the amount of \$110.00 is enclosed to cover the required extension fee. Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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November 200

Robert E. Mates

Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 2 5 day of November, 2001.

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Signature

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